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DOCKETED: STMI07-02357  
Customer No.: 23990

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. : 7,172,980 B2  
Inventor(s) : Joaquin Torres et al.  
Issued : February 6, 2007  
Title : PROCESS FOR FABRICATING AN INTEGRATED  
ELECTRONIC CIRCUIT THAT INCORPORATES AIR GAPS

Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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of Correction

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2. Certificate of Correction (in duplicate); and
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Date: April 27, 2007 Kathy Hamilton  
Mailer  
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**REQUEST FOR CERTIFICATE OF CORRECTION**

Certain clerical or typographical errors were noted in the above-identified patent as indicated on the attached Certificate of Correction.

Applicant respectfully requests that a Certificate of Correction be issued pursuant to 37 C.F.R. §1.322. The Commissioner is hereby authorized to charge any fees associated with this communication or credit any overpayment to Deposit Account Number 50-0208.

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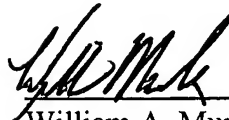
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ATTORNEY DOCKET NO.: STMI07-02357  
PATENT NO.: 7,172,980 B2

Respectfully submitted,

MUNCK BUTRUS, P.C.

Date: April 27, 2007



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MAY 15 7 2007

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PATENT NO. : 7,172,980 B2

DATED : February 6, 2007

INVENTOR(S) : Joaquin Torres, Vincent Arnal and Laurent Gosset

It is certified that error appears in the above-identified patent and that said Letters Patent  
is hereby corrected as shown below:

Column 4, line 60, delete "image processing system" and replace with -- integrated circuit or semiconductor  
processing system --.

**MAILING ADDRESS OF SENDER:**

Docket Clerk  
P.O. Drawer 800889  
Dallas, Texas 75380

PATENT NO. 7,172,980 B2

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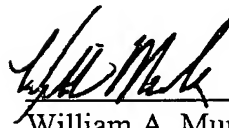
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PTO/SB/44 (04-04)

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